REMARKS

Reconsideration of the application, in view of the following amendments and remarks is respectfully requested.

The Examiner rejects Claims 1-25 under 35 U.S.C. 101 because the claimed invention is directed to non-statutory subject matter.

Claim 11 has been amended in order to recite a post-delay circuit coupled to the mapping system for generating a first delay signal for controlling a signal receiver for generating a second delay for controlling a single transmitter, which obviates the Examiner's '101 rejection.

The Examiner rejects Claims 1, 17 and 23 under 35 U.S.C. 102(b) as being clearly anticipated by Normile et al. The Examiner rejects Claims 1, 17 and 23 under 35 U.S.C. 102(e) as being clearly anticipated by Blumenthal et al or by Klein et al. The Examiner rejects Claim 2-10, 18, 20 and 24-25 under U.S.C. 103(a) as being unpatentable over Normile et al. The Examiner rejects Claims 2-10, 18-20 and 24-25 under 35 U.S.C. 103(a) as being unpatentable over Blumenthal et al or Klein et al.

The Examiner has not made any art rejection against Claims 11-17 or 21-22. These claims have been amended in order to overcome the Examiner's '101 rejection. Accordingly, these claims should now be in condition for allowance.

Accordingly, Applicants believe that the application, as amended, is in condition for allowance, and such action is respectfully requested.

Respectfully submitted by, Texas Instruments Incorporated

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